|  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Instruction** | **RTL** | **Registers** | | | | | | | **Memory** | | | |
| **A** | **B** | **C** | **PC** | **IR** | **MAR** | **MBR** | **14 (Q)** | **15 (R)** | **16 (S)** | **17 (T)** |
| Initial | - | ?? | ?? | ?? | 00 | 21 | ?? | ?? | 3A | 19 | FD | 00 |
| LOAD S  (21) | MAR ← S  MBR ← M[MAR]  A ← MBR  PC ← PC + 2  IR ← M[PC] |  |  |  |  |  |  |  |  |  |  |  |
| MOV C, A  (10) | C ← A  PC ← PC + 2  IR ← M[PC] |  |  |  |  |  |  |  |  |  |  |  |
| LOAD R  (21) | MAR ← R  MBR ← M[MAR]  A ← MBR  PC ← PC + 2  IR ← M[PC] |  |  |  |  |  |  |  |  |  |  |  |
| MOV B, A  (10) | B ← A  PC ← PC + 2  IR ← M[PC] |  |  |  |  |  |  |  |  |  |  |  |
| LOAD Q  (21) | MAR ← Q  MBR ← M[MAR]  A ← MBR  PC ← PC + 2  IR ← M[PC] |  |  |  |  |  |  |  |  |  |  |  |
| SUB A, B  (50) | A ← A - B  PC ← PC + 2  IR ← M[PC] |  |  |  |  |  |  |  |  |  |  |  |
| ADD A, C  (40) | A ← A + C  PC ← PC + 2  IR ← M[PC] |  |  |  |  |  |  |  |  |  |  |  |
| STORE T  (31) | MAR ← T  MBR ← A  M[MAR] ← MBR  PC ← PC + 2  IR ← M[PC] |  |  |  |  |  |  |  |  |  |  |  |

For each instruction in the program, above, fill out the entire row of register and memory values after that instruction executes. Highlight in **bold** and/or red any values that have changed because of this current instruction.